Cambridge International AS & A Level

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COMPUTER SCIENCE

9618/11

Paper 1 Theory Fundamentals

October/November 2022

1 hour 30 minutes

You must answer on the question paper.

No additional materials are needed.

INSTRUCTIONS

- Answer all questions.
- Use a black or dark blue pen.
- Write your name, centre number and candidate number in the boxes at the top of the page.
- Write your answer to each question in the space provided.
- Do **not** use an erasable pen or correction fluid.
- Do not write on any bar codes.
- You may use an HB pencil for any diagrams, graphs or rough working.
- Calculators must not be used in this paper.

INFORMATION

- The total mark for this paper is 75.
- The number of marks for each question or part question is shown in brackets [].
- No marks will be awarded for using brand names of software packages or hardware.

This document has 20 pages. Any blank pages are indicated.

1 (a) (i) Convert the unsigned binary integer into denary.

00100111

	Answer												[1]
(ii												 	[']
(11) Convert the	Dillary C	Jude	a De	UIIIa	i (bC	ווו (טי	to de	ziiai y	•			
					00	100	111						
	Answer											 	[1]
(iii) Convert the	8-bit two	o's co	mple	men	nt bin	arv ir	nteae	er into	denar	V		
(, σοινοιταίο	o bit tirt						ogc	, ,,,,,	, aona.	<i>,</i> .		
					111	100	111						
	Answer											 	[1]
(b) P	erform the follo	wing bin	ary s	ubtra	action	n. Sh	OW y	our v	vorkii	ng.			
,		J	,				,			J			
			1	0	1	1	0	0	1	1			
			0	1	1	1	0	1	0	1			
		_				ı	U		U				

[2]

(c)	Give	e one similarity and two differences between the ASCII and Unicode character sets.	
	Sim	ilarity	
	•••••		
	Diffe	erence 1	
	Diffe	erence 2	
			 [3
(d)	Sou	nd samples are recorded and saved in a file.	
	(i)	State what is meant by sampling rate.	
			. [1
	(ii)	Explain the effect of increasing the sampling resolution on the sound file.	
			•••••
			. [2

2 Draw **one** line from each security feature to its most appropriate description.

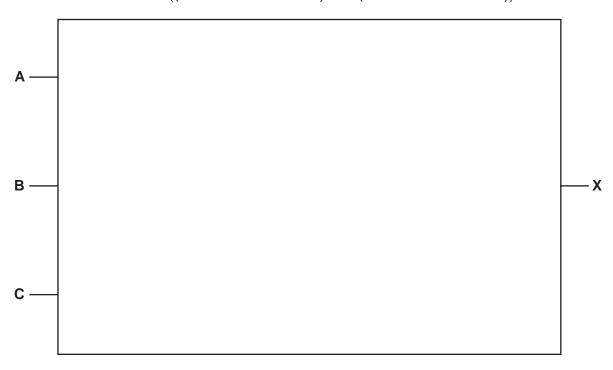
Security feature Description converts data to an alternative form firewall redirects a user to a fake website pharming verifies the authenticity of data anti-virus software scans files on the hard drive for malicious software encryption accepts or rejects incoming and outgoing packets based

[4]

on criteria

3 (a) Draw a logic circuit for the logic expression:

X = NOT ((NOT A AND NOT B) OR (NOT B AND NOT C))



(b) Complete the truth table for the logic expression:

X = NOT ((NOT A AND NOT B) OR (NOT B AND NOT C))

A	В	С	Working space	X
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

[2]

[2]

4 A photographer creates a relational database to store data about photographs taken at birthday parties.

The database, PHOTOGRAPHS, stores details of the customer, the party, the photographs taken and the cameras used.

The photographer has several cameras that are used for taking the photographs at the parties.

Each camera has a specific lens type (for example, XY32Z) and lighting type (for example, F1672).

Data about each photograph is stored in the database including the party at which it was taken, the time it was taken and the camera used.

The database has these four tables:

```
CUSTOMER(CustomerID, FirstName, LastName, Telephone)

PARTY(PartyID, CustomerID, PartyDate, StartTime)

PHOTO_DATA(PhotoID, PartyID, TimeTaken, CameraID)

CAMERA_DATA(CameraID, LensType, LightingType)
```

(a) Complete the entity-relationship (E-R) diagram for the database PHOTOGRAPHS.

PARTY CUSTOMER

PHOTO_DATA CAMERA_DATA

[3]

	d Normal Form (3N	NF). 							
Γhe	table shows so	ome sample data	for the table PHOTO	_DATA.					
	PhotoID	PartyID	TimeTaken	CameraID	_				
	ST23-56	BD987	08:34	NIK-02					
	ST23-57	BD987	08:55	NIK-02					
	ST23-60	BC08	09:01	CAN-01					
	ST23-61	BC08	10:23	CAN-12					
	ST23-62	BC08	10:56	NIK-01					
			Give an example of		TO_DATA.				
	Complete the Structured Query Language (SQL) script to display the total numb photographs that have been taken using a camera with a camera ID starting with CA								
	SELECT		(

(d)	Write an SQL script to include two new fields in CAMERA_DATA to store the number photographs currently on the camera and the date the camera was last used.	
		[3]

Question 5 begins on page 10.

(d) A	Occident December 11st (ODII)			
СО	Central Processing Unit (CPU) components.	intains severai s	peciai purpose reg	gisters and
(i)	State the role of the following regis	sters.		
	Program Counter (PC)			
	Index Register (IX)			
	Status Register (SR)			
(ii)	Tick (✓) one box in each row to id	antify the system	hus used by each (CDI compor
(11)	TICK (V) One box in each row to id	- Indian the system	bus used by each v	CFO Compor
	CPU component	Data bus	Address bus	Control b
	System clock			
	Memory Address Register (MAR))		
(iii)	Describe the purpose of the Contr	al Unit (CUI) in a C	NDL I	

(c)	Describe the purpose of an interrupt in a computer system.
	[2]
(d)	Identify two causes of a software interrupt.
	1
	2
	[2]

6 The following table shows part of the instruction set for a processor. The processor has one general purpose register, the Accumulator (ACC), and an Index Register (IX).

Instruction Opcode Operand		Explanation				
		Explanation				
LDM	#n	Immediate addressing. Load the number n to ACC				
LDD	<address></address>	Direct addressing. Load the contents of the location at the given address to ACC				
LDX <address></address>		Indexed addressing. Form the address from <address> + the contents of the index register. Copy the contents of this calculated address to ACC</address>				
LDR	#n	Immediate addressing. Load the number n to IX				
STO	<address></address>	Store contents of ACC at the given address				
ADD	<address></address>	Add the contents of the given address to the ACC				
ADD	#n	Add the denary number n to the ACC				
INC	<register></register>	Add 1 to the contents of the register (ACC or IX)				
CMP	#n	Compare the contents of ACC with number n				
JPE	<address></address>	Following a compare instruction, jump to <address> if the compare was True</address>				
JPN	<address></address>	Following a compare instruction, jump to <address> if the compare was False</address>				
OUT		Output to the screen the character whose ASCII value is stored in ACC				
END		Return control to the operating system				

<address> can be an absolute or a symbolic address

[#] denotes a denary number, e.g. #123

B denotes a binary number, e.g. B01001101

Output

ACC

(a) The current contents of main memory and selected values from the ASCII character set are given.

IX

(i) Trace the program currently in memory using the trace table.

Instruction

address

Address	Instruction
77	LDR #0
78	LDX 110
79	CMP #35
80	JPE 92
81	ADD 100
82	STO 101
83	LDM #1
84	ADD 100
85	STO 100
86	INC IX
87	LDX 110
88	CMP #35
89	JPN 81
90	LDD 100
91	ADD #48
92	OUT
93	END
	کے
100	0
101	0
	لم
110	66
111	65
112	35

ASCII value	Character
49	1
50	2
51	3
52	4
کے	لم
65	А
66	В
67	С
68	D

addicoo		100	101	110	111	112	
		0	0	66	65	35	

Memory address

(ii) The following instructions are repeated for your reference.

Ins	truction	Evalenation
Opcode	Operand	Explanation
LDD	<address></address>	Direct addressing. Load the contents of the location at the given address to ACC
STO	<address></address>	Store contents of ACC at the given address

State the purpose of this part of an assembly language program.

LDI	D 100	
STO	0 165	
LDI	D 101	
STO	0 100	
LDI	D 165	
STO	0 101	
		[1]

Question 6(b) begins on page 16.

(b) The following table shows another part of the instruction set for the processor.

Opcode Operand AND #n Bitwise AND operation of the contents of ACC with the operand AND Bn Bitwise AND operation of the contents of ACC with the operand XOR #n Bitwise XOR operation of the contents of ACC with the binary number of the contents of ACC with the operand XOR Bn Bitwise XOR operation of the contents of ACC with the binary number of the contents of ACC with the operand of the operand of the contents of ACC with the operand of the operand of the contents of ACC with the operand of the operand of the operand of the contents of ACC with the operand of the operand of the contents of ACC with the operand of the operand of the contents of ACC with the operand o	AND AND XOR		rand					Explana	ation		
Bitwise AND operation of the contents of ACC with the binary number SOR #n Bitwise XOR operation of the contents of ACC with the operand Bitwise XOR operation of the contents of ACC with the binary number OR #n Bitwise OR operation of the contents of ACC with the operand OR Bn Bitwise OR operation of the contents of ACC with the binary number or Bitwise OR operation of the contents of ACC with the binary number of LSR #n Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end standards a denary number, e.g. #123 Bits in ACC are: 1	AND	#n						-			
Bitwise XOR operation of the contents of ACC with the operand	XOR			Bitwis	e AND (operatio	n of the	content	s of AC	C with th	ne operand
Bitwise XOR operation of the contents of ACC with the binary number OR #n Bitwise OR operation of the contents of ACC with the operand OR Bn Bitwise OR operation of the contents of ACC with the binary number of LSR #n Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end standardess can be an absolute or a symbolic address denotes a denary number, e.g. #123 and denotes a binary number, e.g. B01001101 (i) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction. XOR B00011111 (ii) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.		Bn		Bitwis	e AND (operatio	n of the	content	s of AC	C with th	ne binary number
OR #n Bitwise OR operation of the contents of ACC with the operand OR Bn Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end address can be an absolute or a symbolic address a denotes a denary number, e.g. #123 of denotes a binary number, e.g. B01001101 (i) The current contents of the ACC are: \[\begin{array}{cccccccccccccccccccccccccccccccccccc	XOR	#n		Bitwis	e XOR	operatio	n of the	content	s of AC	C with th	ne operand
Bn Bitwise OR operation of the contents of ACC with the binary number of Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end address a denary number, e.g. #123 denotes a binary number, e.g. 801001101 (i) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction. XOR B00011111 (ii) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.		Bn		Bitwis	e XOR	operatio	n of the	content	s of AC	C with th	ne binary number
LSR #n Bits in ACC are shifted logically n places to the right. Zeros are introduced on the left-hand end address> can be an absolute or a symbolic address denotes a denary number, e.g. #123 denotes a binary number, e.g. B01001101 (i) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction. XOR B00011111 (ii) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.	OR	#n		Bitwis	e OR or	peration	of the c	ontents	of ACC	with the	operand
introduced on the left-hand end address> can be an absolute or a symbolic address denotes a denary number, e.g. #123 denotes a binary number, e.g. B01001101 (i) The current contents of the ACC are: \[\begin{array}{c ccccccccccccccccccccccccccccccccccc	OR	Bn		Bitwis	e OR or	peration	of the c	ontents	of ACC	with the	binary number n
denotes a denary number, e.g. #123 denotes a binary number, e.g. B01001101 (i) The current contents of the ACC are: \[\begin{array}{c ccccccccccccccccccccccccccccccccccc	LSR	#n							ces to th	ne right.	Zeros are
Show the result after the execution of the following instruction. XOR B00011111 (ii) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.	(i)	The c	urrent c	ontents	of the A	.CC are:					
XOR B00011111 (ii) The current contents of the ACC are: 1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.			1	0	0	1	0	0	1	1	
1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.											
1 0 0 1 0 0 1 1 Show the result after the execution of the following instruction.]
Show the result after the execution of the following instruction.	(ii)	The c	urrent c	ontents	of the A	CC are:					
			1	0	0	1	0	0	1	1	
		Show	the res	ult after				_	nstructio	n.	

						•					
(iii)	The c	urrent c	ontents	of the A	CC are:						
		1	0	0	1	0	0	1	1		
	Show	the res	ult after	the exe	cution o	f the foll	owing	instruction	on.		
				OI	R B11	00110	0.0				
										_	
(iv)	The c	urrent c	ontents	of the A	CC are:			'			[1]
			I			I	Ι				
		1	0	0	1	0	0	1	1		
	Show	the res	ult after	the exe	cution o	f the foll	owing	instruction	on.		
					LSR	#2					
											[1]
			ore box d pass o					ether the	task is	performed in the	first
			Task	(First p	ass	Second pass	
Remove	comm	nents.									
D 1 11		I- I I			P						

Task	First pass	Second pass
Remove comments.		
Read the assembly language program one line at a time.		
Generate the object code.		
Check the opcode is in the instruction set.		

[2]

7 (a)	State two benefits to a programmer of using Dynamic Link Library (DLL) files.
	1
	2
	[2]
(b)	Memory management is one of the tasks performed by an Operating System (OS).
	Describe the ways in which memory management organises and allocates Random Access Memory (RAM).
	[2]
(c)	An Operating System may include a utility program to compress text files.
	Describe one appropriate method of compressing a text file.
	[3]
(d)	Explain the reasons why increasing the amount of cache memory can improve the performance of a CPU.
	[2]
(e)	State the name of a peripheral device port that provides a physical connection in the computer for each of these peripherals.
	3D printer
	Monitor[2]

	scribe how Carrier Sense Multiple Access/Collision Detection (CSMA/CD) is used work.	l in a bus
		[4]
	ny modern televisions are examples of embedded systems.	
	ny modern televisions are examples of embedded systems. Explain why these televisions are embedded systems.	
	Explain why these televisions are embedded systems.	
(a)	Explain why these televisions are embedded systems.	
(a)	Explain why these televisions are embedded systems.	
(a)	Explain why these televisions are embedded systems. Embedded systems use Electrically Erasable Programmable ROM (EEPROM).	
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